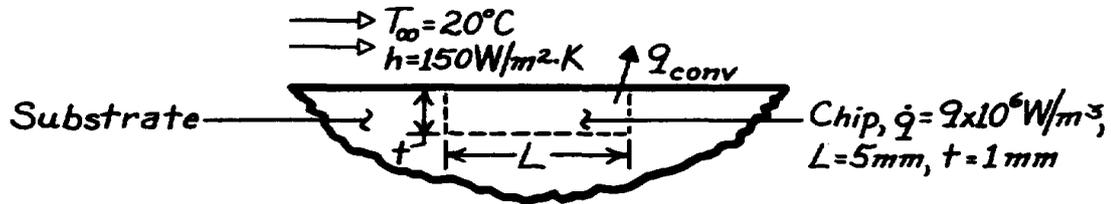


PROBLEM 5.27

KNOWN: Dimensions and operating conditions of an integrated circuit.

FIND: Steady-state temperature and time to come within 1°C of steady-state.

SCHEMATIC:



ASSUMPTIONS: (1) Constant properties, (2) Negligible heat transfer from chip to substrate.

PROPERTIES: Chip material (given): $\rho = 2000 \text{ kg/m}^3$, $c = 700 \text{ J/kg}\cdot\text{K}$.

ANALYSIS: At steady-state, conservation of energy yields

$$\begin{aligned} -\dot{E}_{\text{out}} + \dot{E}_{\text{g}} &= 0 \\ -h(L^2)(T_f - T_\infty) + \dot{q}(L^2 \cdot t) &= 0 \\ T_f &= T_\infty + \frac{\dot{q}t}{h} \end{aligned}$$

$$T_f = 20^\circ\text{C} + \frac{9 \times 10^6 \text{ W/m}^3 \times 0.001 \text{ m}}{150 \text{ W/m}^2 \cdot \text{K}} = 80^\circ\text{C} \quad <$$

From the general lumped capacitance analysis, Equation 5.15 reduces to

$$\rho(L^2 \cdot t)c \frac{dT}{dt} = \dot{q}(L^2 \cdot t) - h(T - T_\infty)L^2.$$

With

$$\begin{aligned} a &\equiv \frac{h}{\rho tc} = \frac{150 \text{ W/m}^2 \cdot \text{K}}{(2000 \text{ kg/m}^3)(0.001 \text{ m})(700 \text{ J/kg}\cdot\text{K})} = 0.107 \text{ s}^{-1} \\ b &\equiv \frac{\dot{q}}{\rho c} = \frac{9 \times 10^6 \text{ W/m}^3}{(2000 \text{ kg/m}^3)(700 \text{ J/kg}\cdot\text{K})} = 6.429 \text{ K/s}. \end{aligned}$$

From Equation 5.24,

$$\exp(-at) = \frac{T - T_\infty - b/a}{T_i - T_\infty - b/a} = \frac{(79 - 20 - 60) \text{ K}}{(20 - 20 - 60) \text{ K}} = 0.01667$$

$$t = -\frac{\ln(0.01667)}{0.107 \text{ s}^{-1}} = 38.3 \text{ s} \quad <$$

COMMENTS: Due to additional heat transfer from the chip to the substrate, the actual values of T_f and t are less than those which have been computed.