

I. Vapor Deposition Polymerization

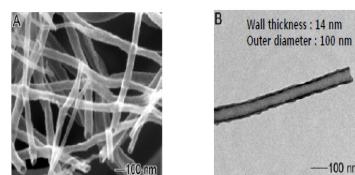
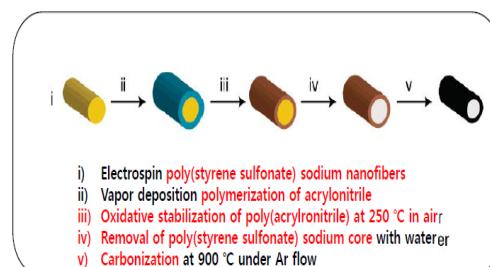
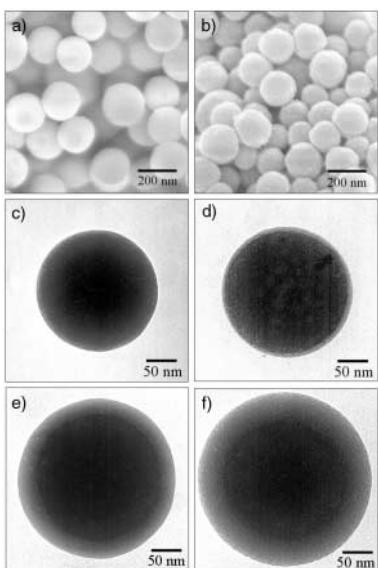
Advantages of Vapor Deposition Polymerization

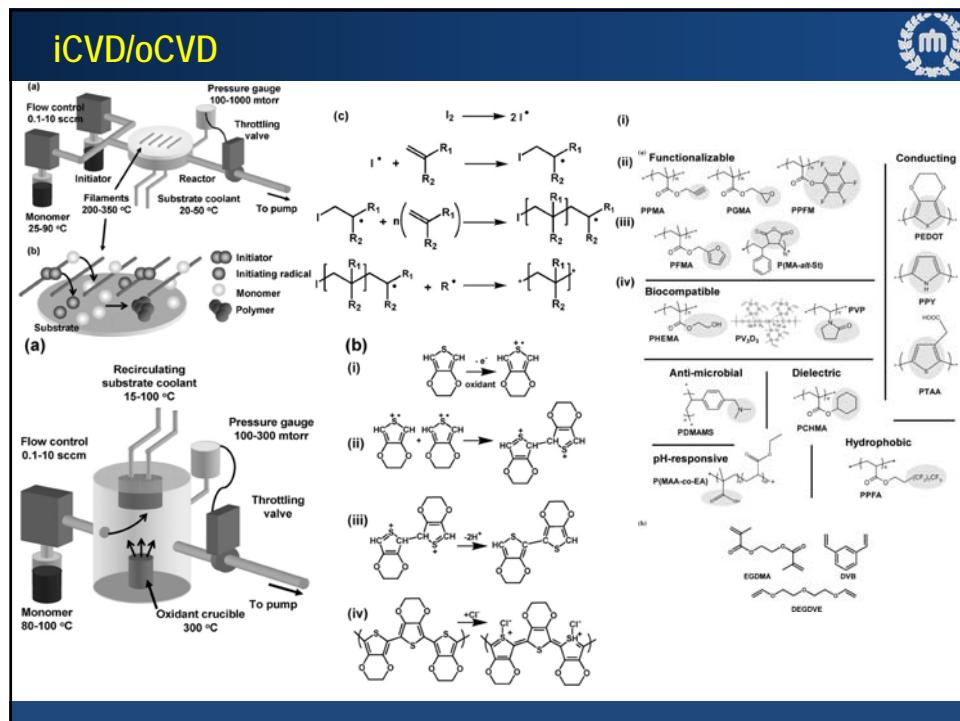
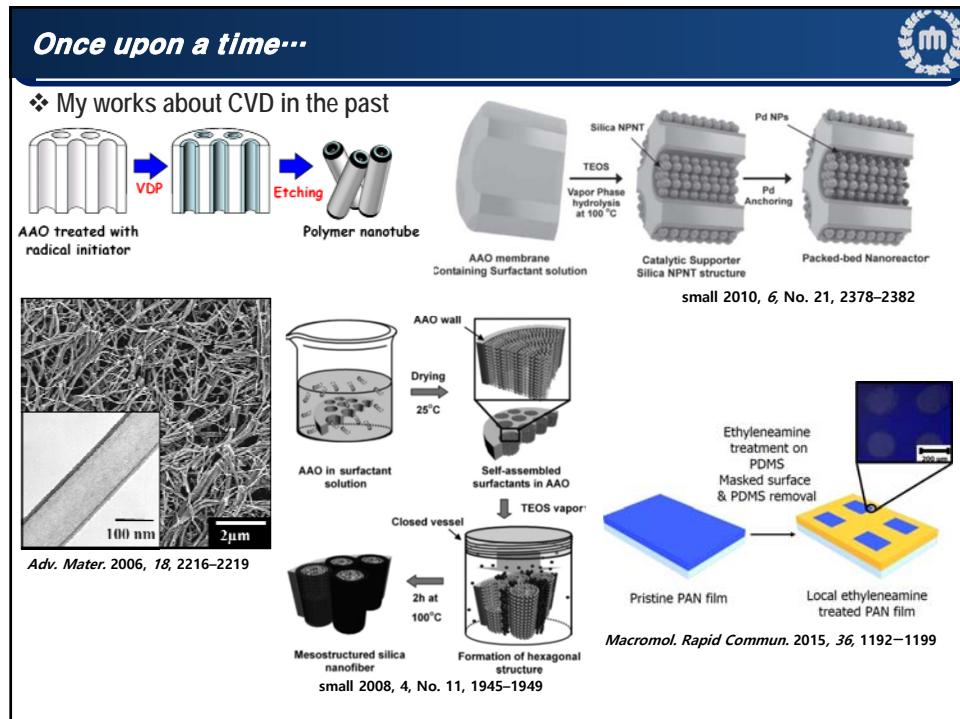
- No Solvent (Environmental Friendly)
- No limitation on Monomers or Materials
- No limitation on surfaces (materials, morphology)
- Relatively Easy Process
- Good Processibility
- Conformal Coating

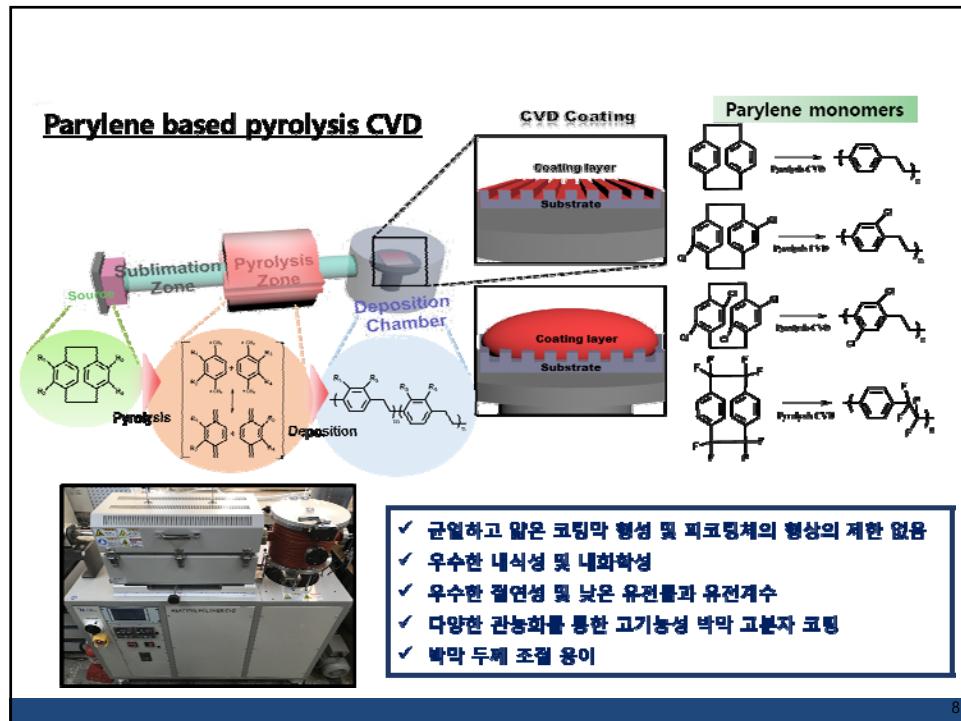
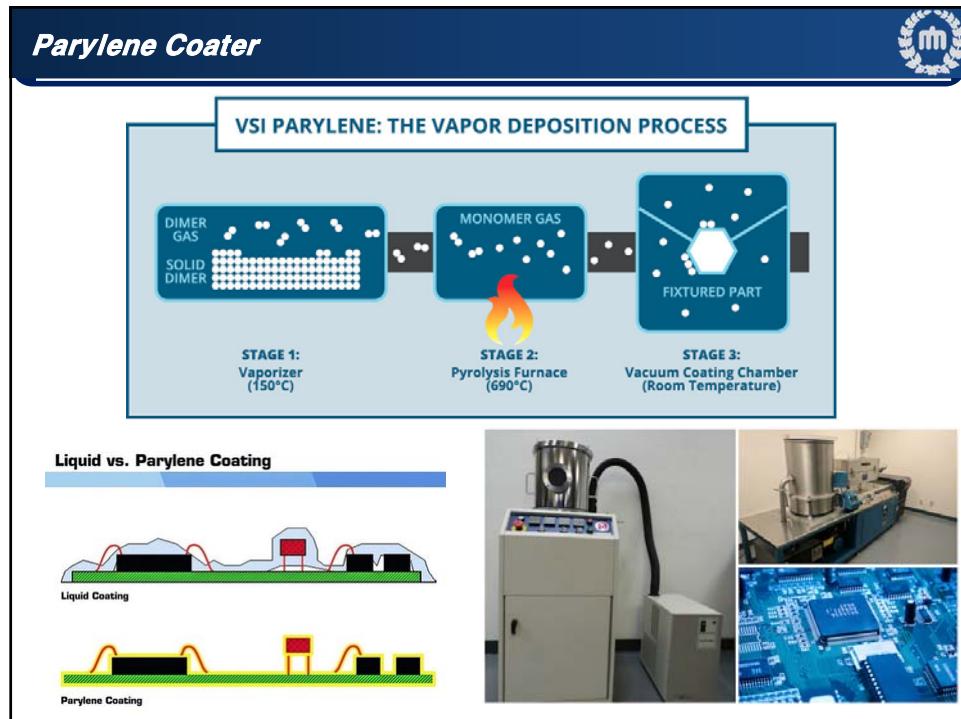
iCVD, oCVD, pyrolysis CVD, Vapor Deposition Polymerization

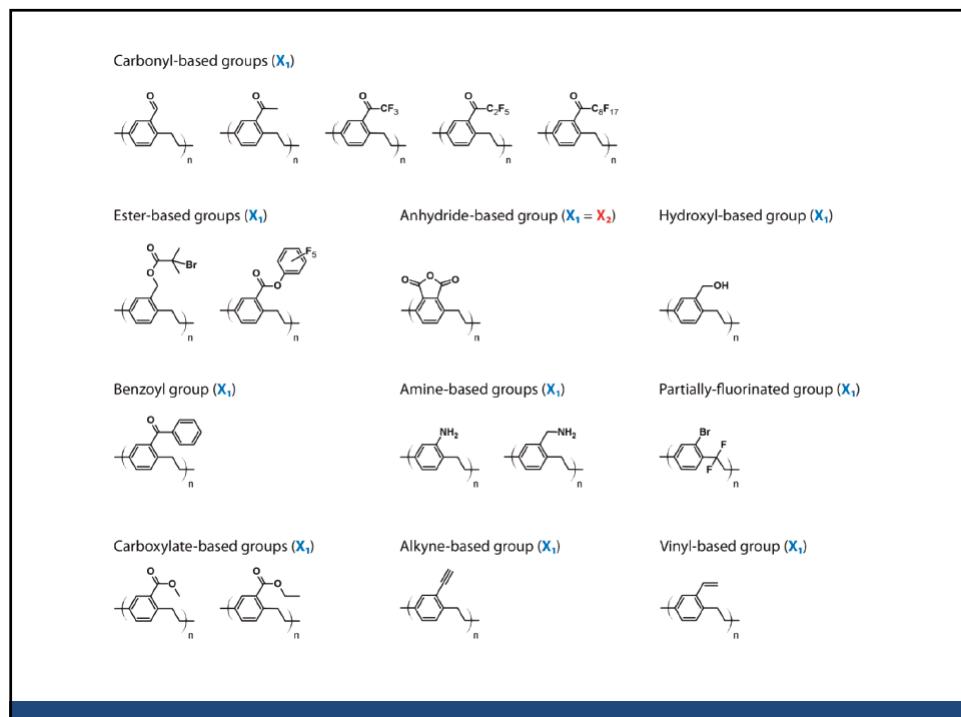
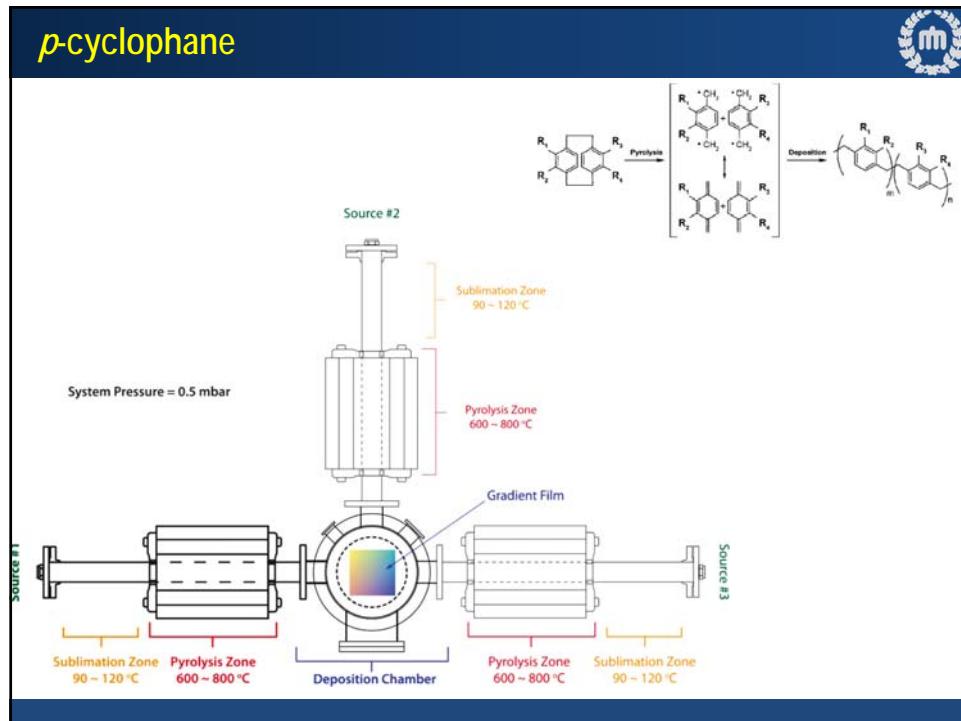
- K. Gleason (MIT)
- J. Lahann (Michigan)

Particle Coating









REVIEW

Hall of Fame Article

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Functionalization of Poly(*para*-xylylene)s—Opportunities and Challenges as Coating Material

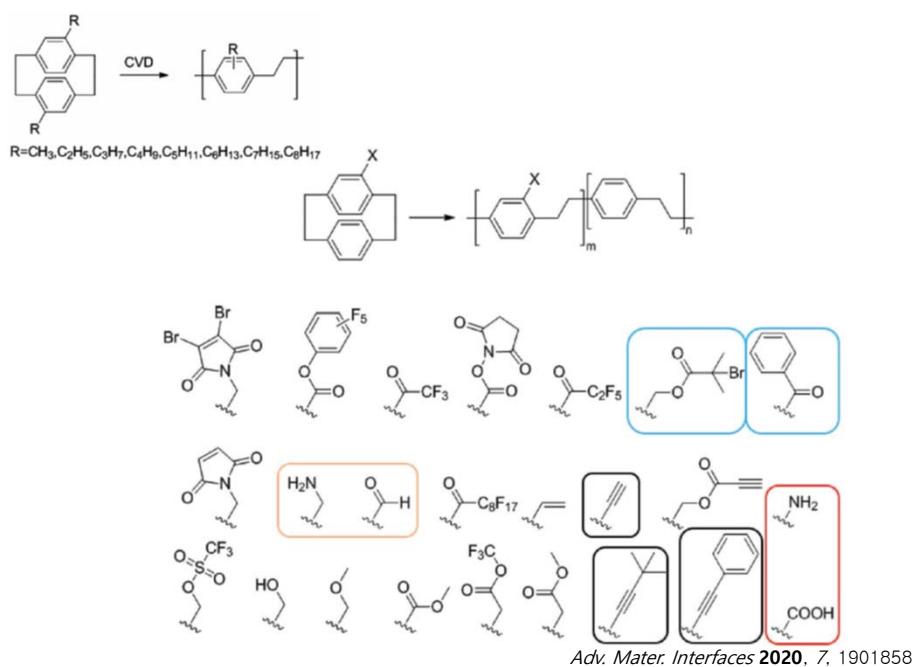
Tobias Moss and Andreas Greiner*

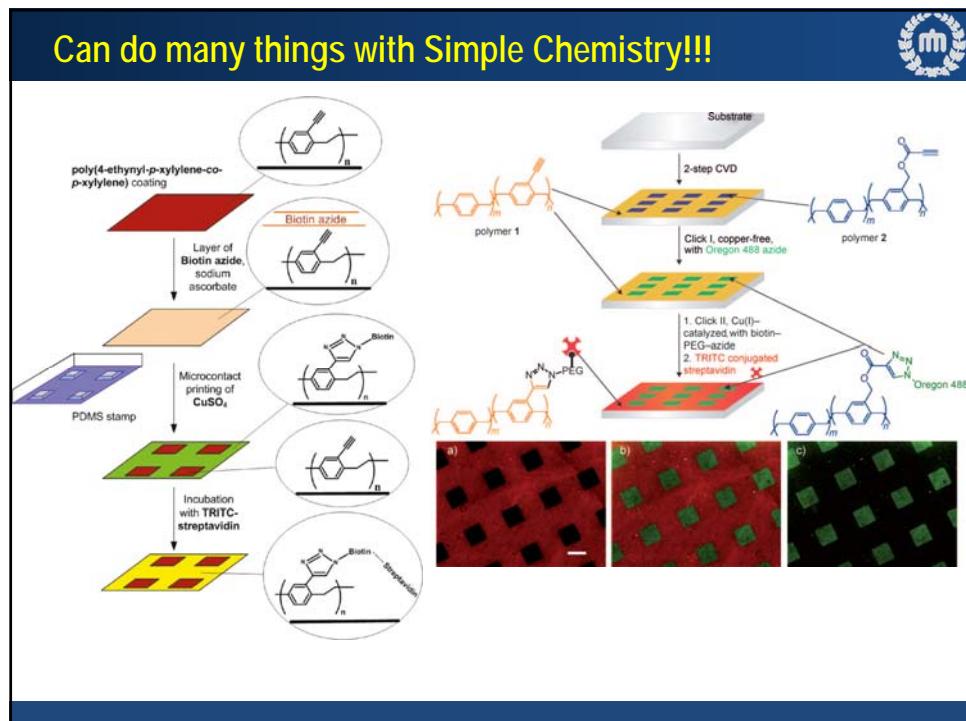
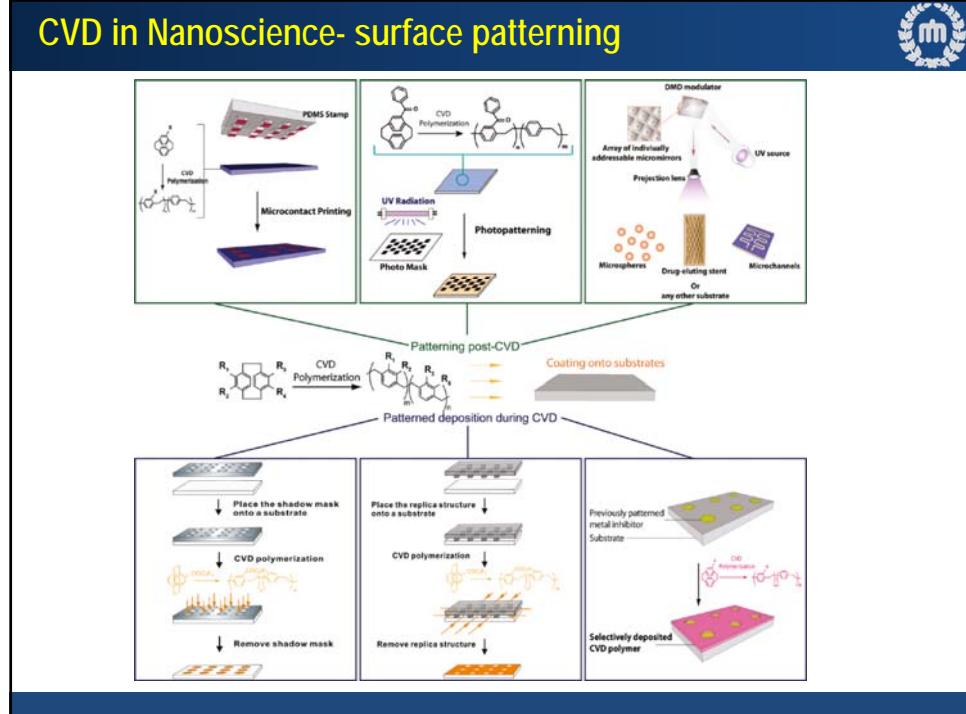
The chemical vapor deposition (CVD) of poly(*para*-xylylene)s (PPX) is an enabling technology for materials design as well as for numerous high-performance applications. Additionally, PPX possesses of a unique set of structure–property relationships that can be tuned over a wide range. Different strategies vary from functionalization of the most used precursor [2,2] paracyclophane to the testing of new precursors and the copolymerization with various monomers. In this review, some recent developments on synthesis and properties of this unique class of polymers, the PPX, are reported.

systems^[14–16] organic light-emitting diodes^[17] or in biological and medical applications^[18–20]. A relatively new area in the PPX research is the formation of reactive coatings. This can be achieved by the introduction of functional groups that are able to react in polymer analogous reactions.^[21–23]

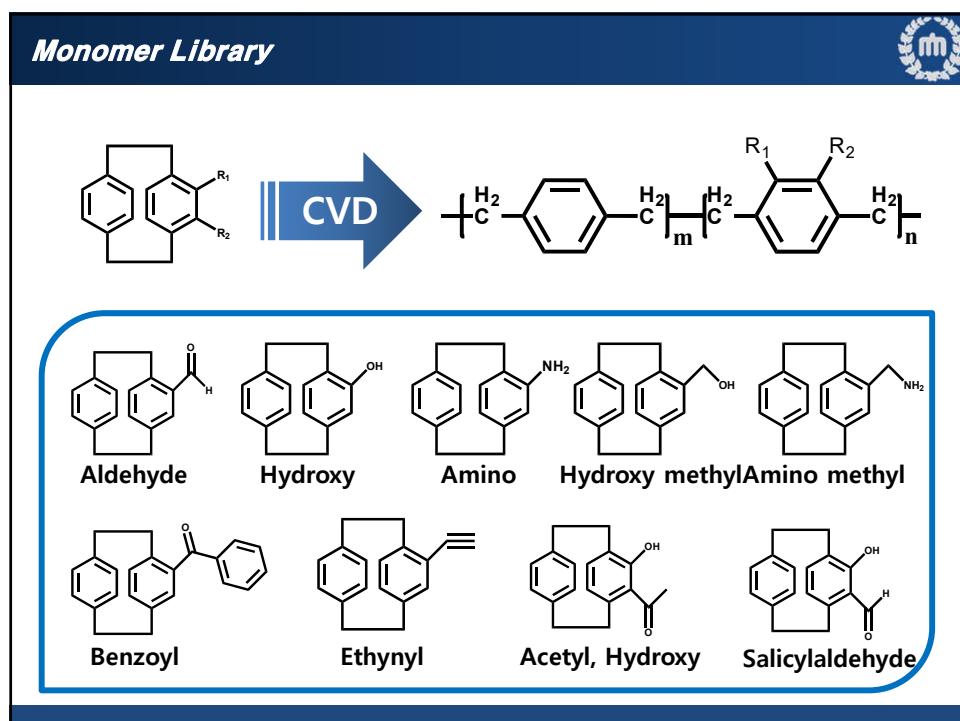
This review should also provide an overview of the opportunities that especially functionalized PPXs made by CVD provide. First, some general remarks and examples of PPX made by CVD will be given before functionalized PPX will be

Adv. Mater. Interfaces 2020, 7, 1901858

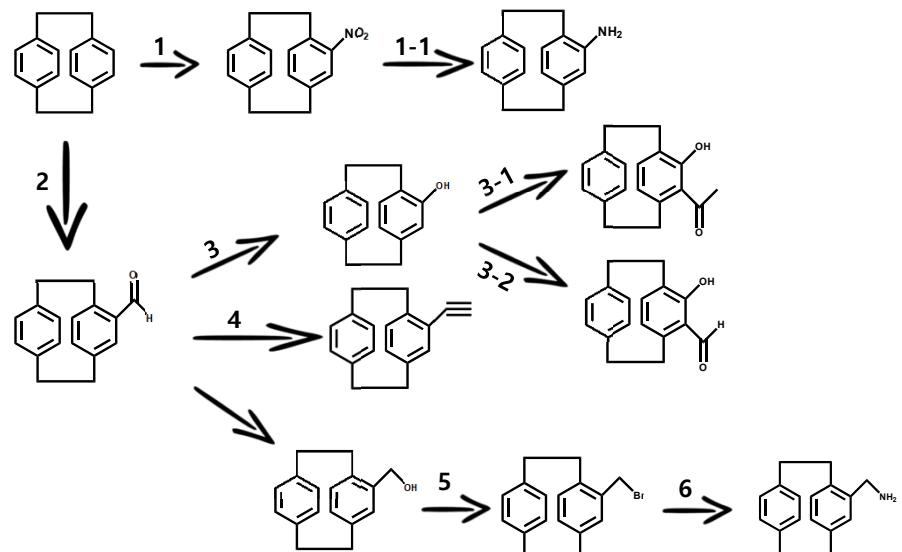




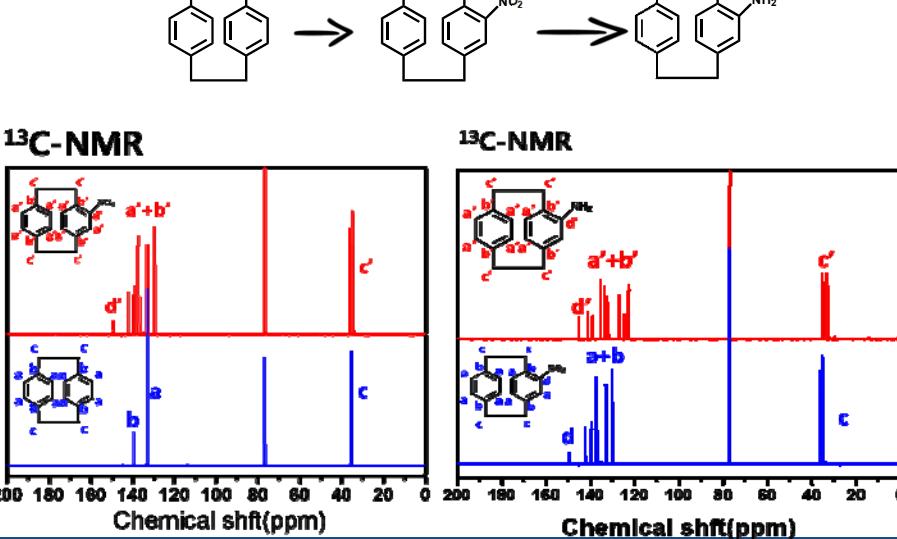
Currently Available Monomers in our Lab



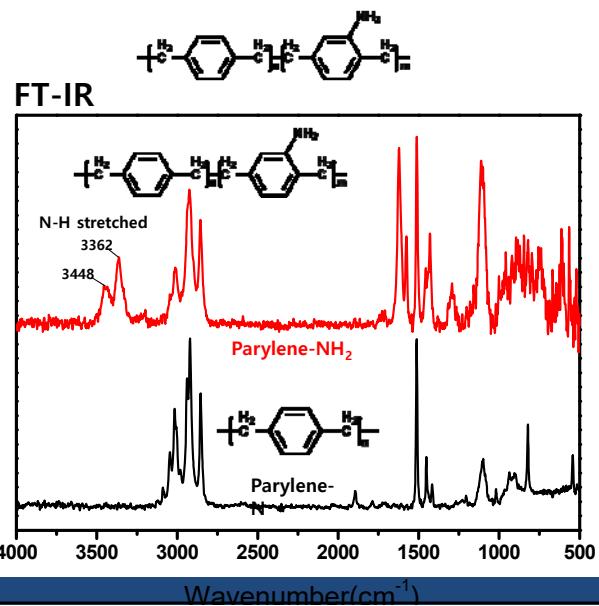
Functionalized Parylene



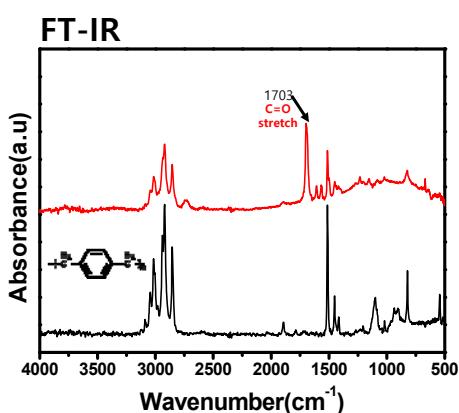
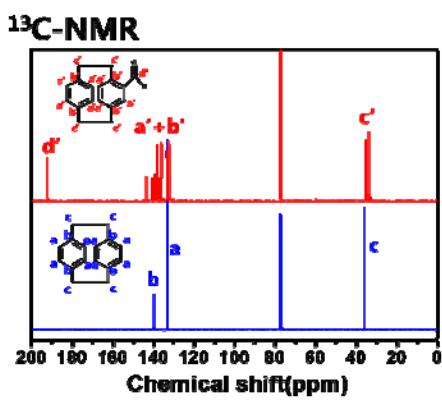
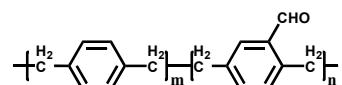
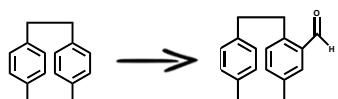
Amine

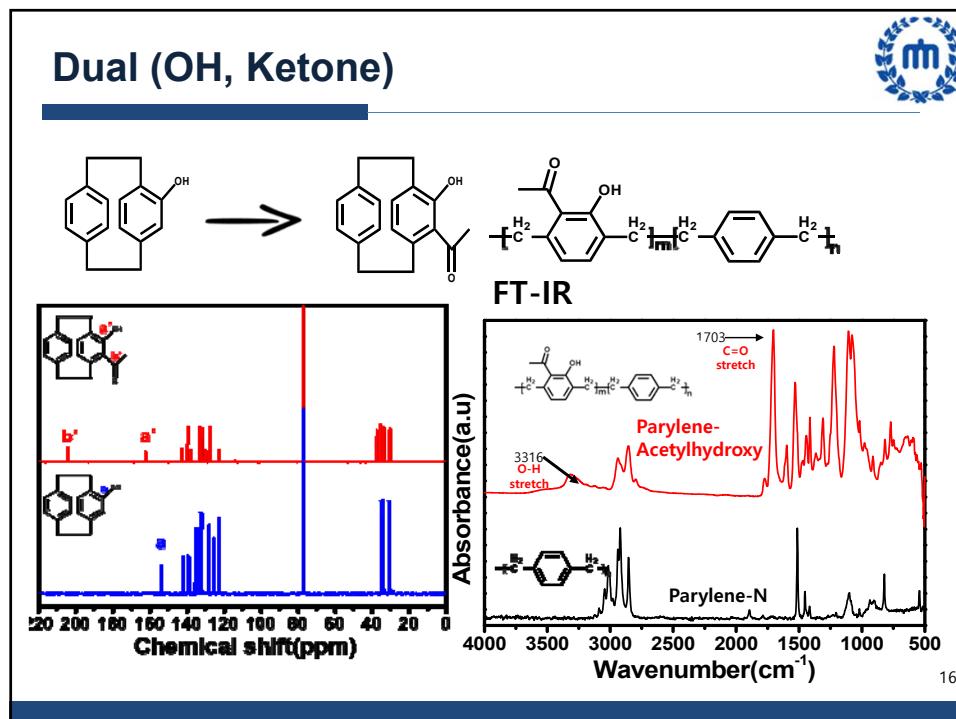
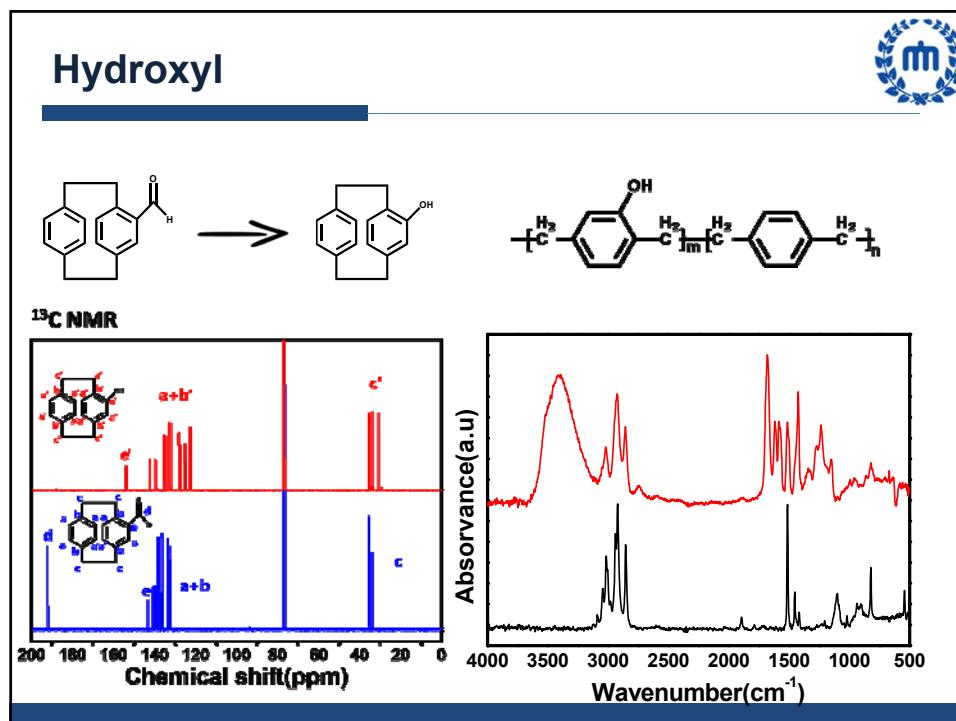


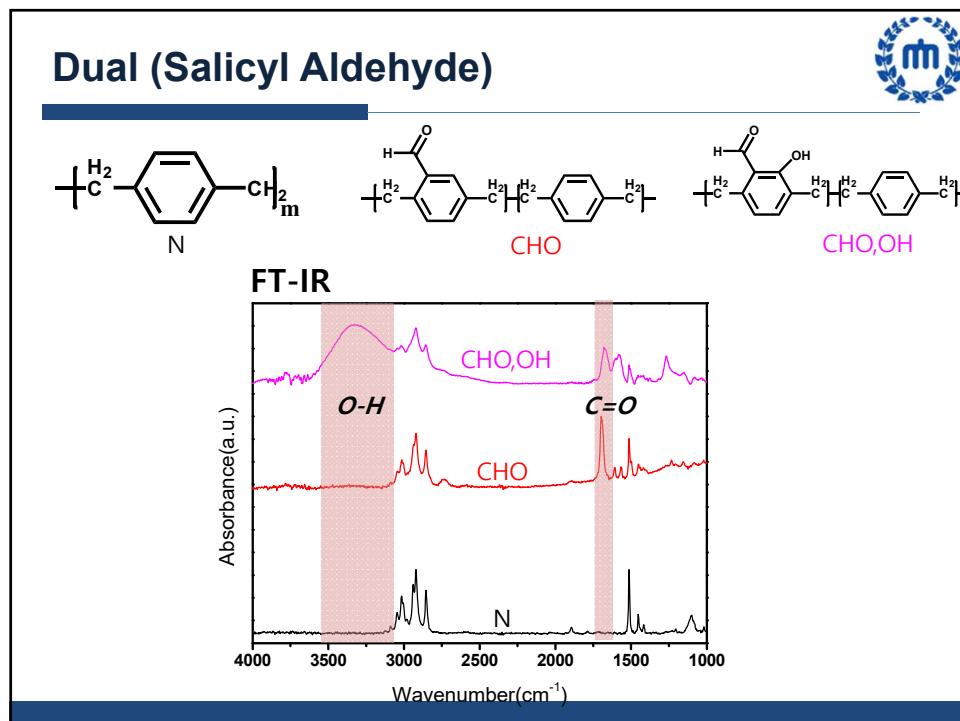
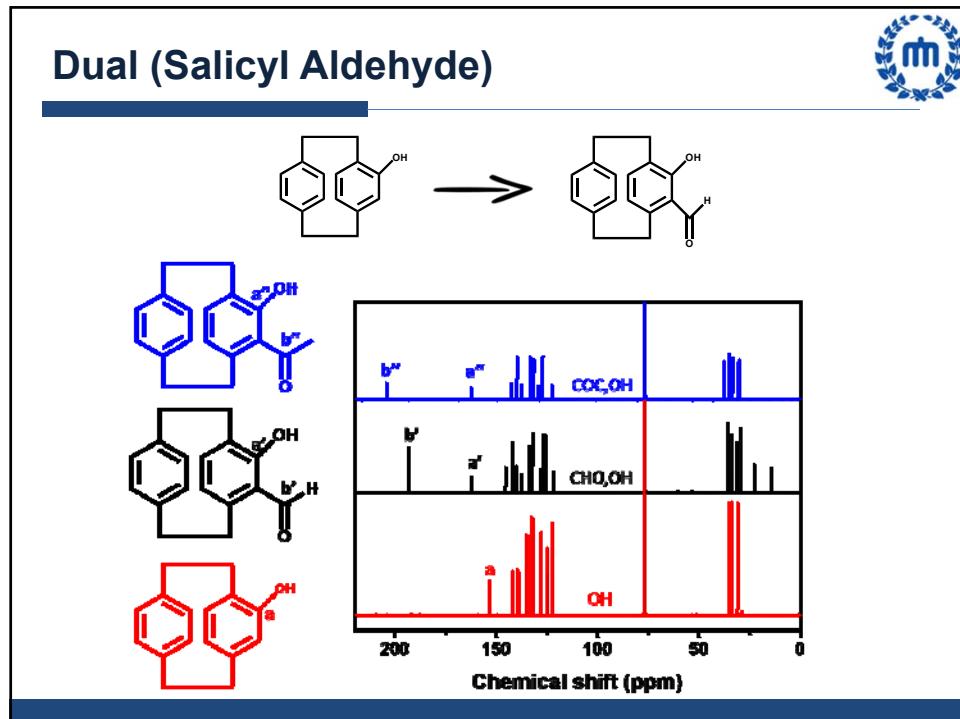
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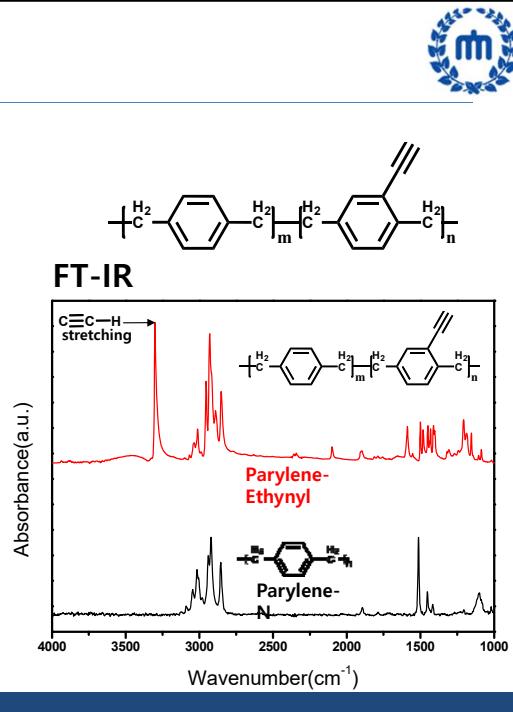
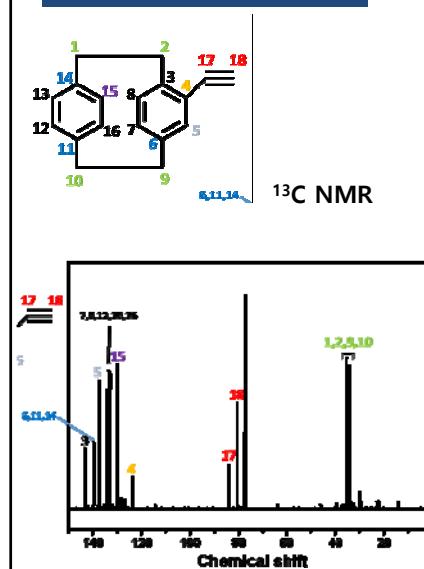
Aldehyde





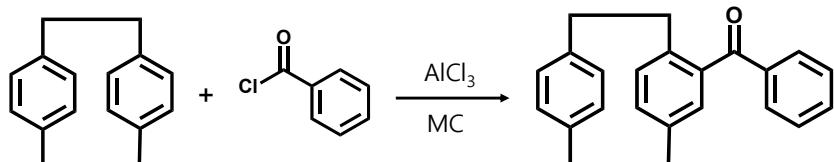


Alkyne

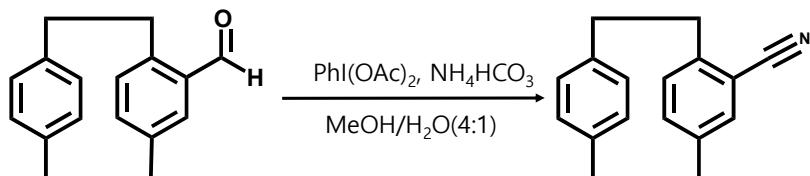


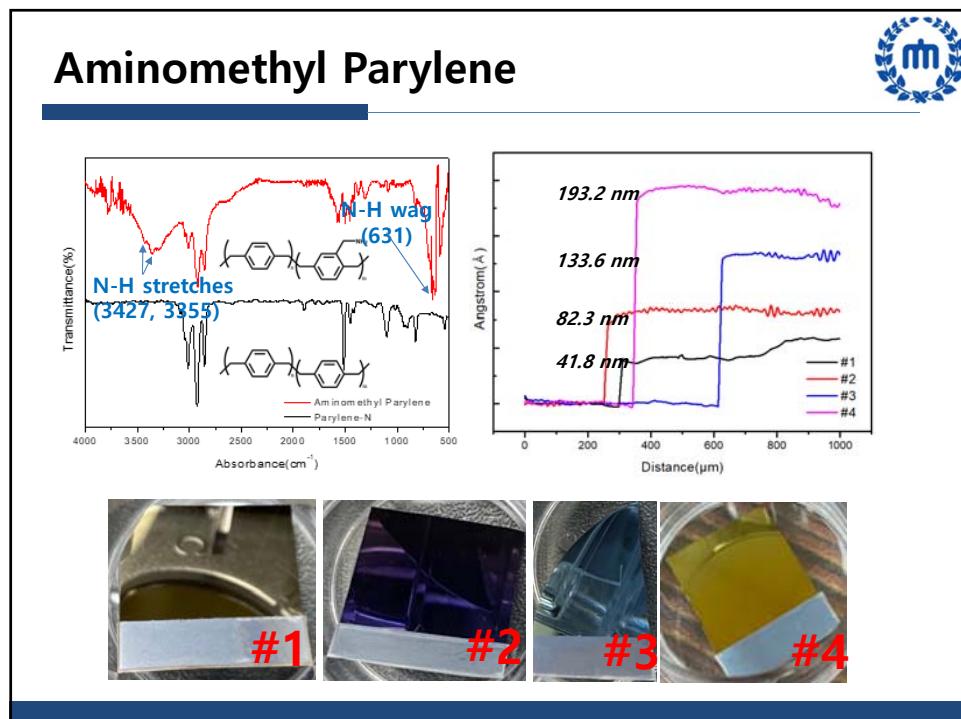
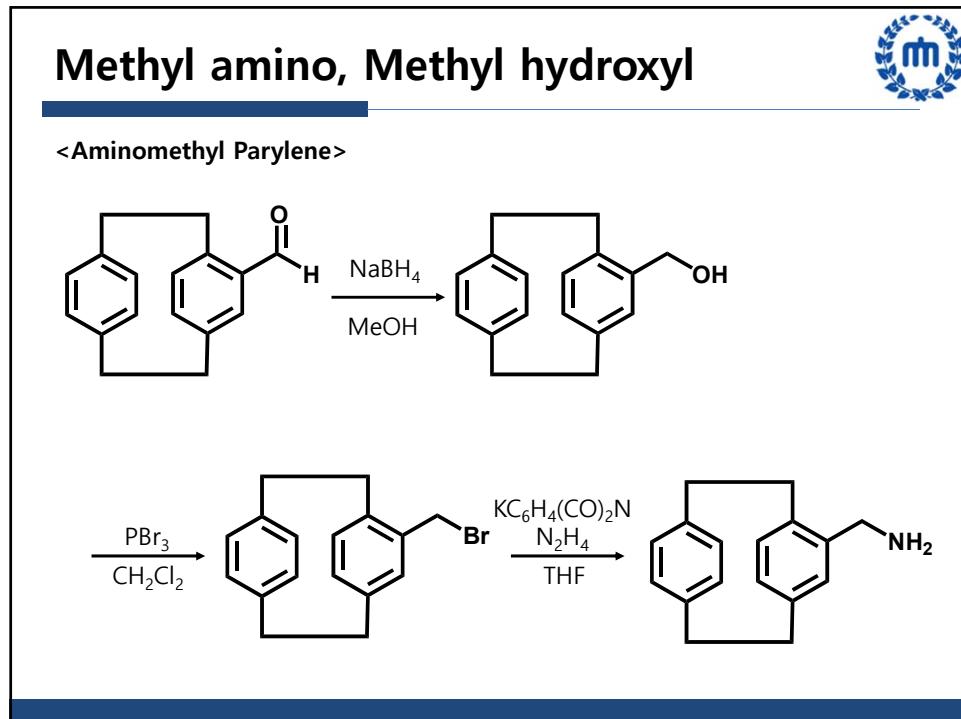
Benzoyl, Cyanide

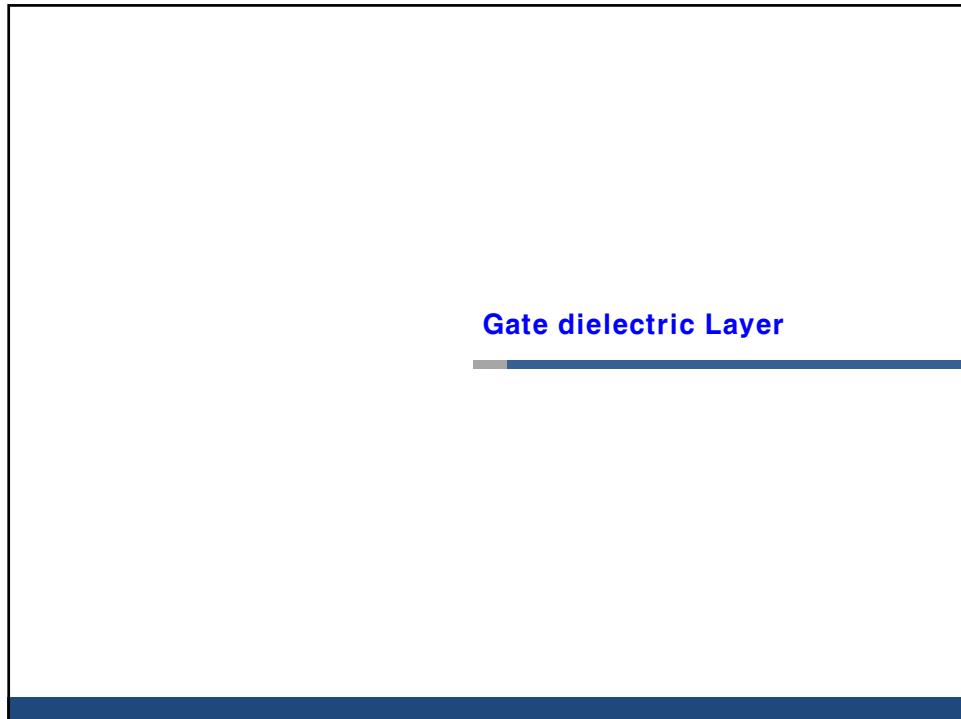
<Benzoyl Parylene>



< Cyanide Parylene >







Parylene works in Electronics

Majors are Encapsulation layer, Protection, and some of Dielectric layers in electronics!

(a) A six-step process diagram for Parylene C film deposition:

- UV light exposure and Mask.
- Platinum deposition (Metal electrodes, tracks and pads).
- Deposition of Parylene C film (Parylene C and Metal).
- UV light exposure and Mask.
- O₂ plasma.
- Peeling-off Parylene C (Hydrophobic Parylene C and Hydrophilic Parylene C) and Fixing the sensing array on PCB (Parylene C flexible array and PCB).

Legend:

- Photosist
- Parylene C film
- Silicon substrate
- Pad opening
- Photosist
- Parylene C
- Metal

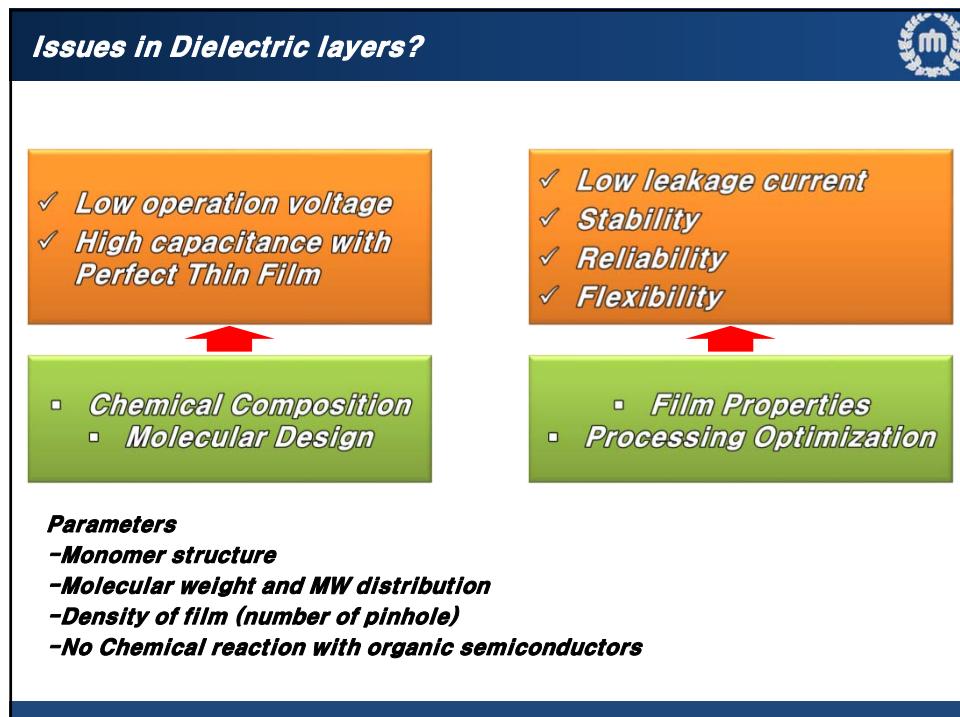
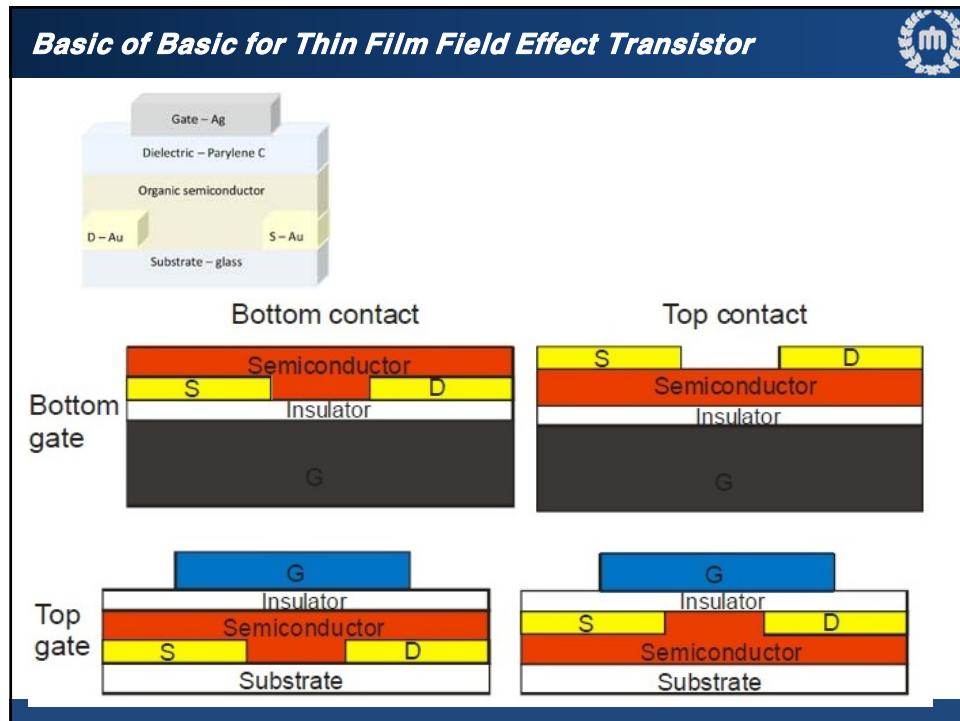
(b) A photograph of a hand holding a clear, flexible Parylene encapsulated device.

(c) A photograph of a hand holding a green printed circuit board (PCB) with a central electronic component.

Liquid Coating

Parylene Coating

PROTECTING ELECTRONICS WITH PARYLENE



Previous works with Parylene as dielectric layer

Parylene based bilayer flexible gate dielectric layer for top-gated organic field-effect transistors

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^a Department of Energy Materials Engineering, Dongguk University, 30, Pildong-ro 1-gil, Jung-gu, Seoul, 04620, Republic of Korea
^b Korea Atomic Energy Research Institute, Daeduk-daero 989-111, Yusong-gu, Daejeon, 34057, Republic of Korea

(a) Schematic diagram of the bilayer dielectric structure. (b) Chemical structures of IDTB and P(NDI2OD-T2). (c) SEM image of the P(NDI2OD-T2) bilayer. (d) Chemical structures of Parylene-C, PS, and PMMA.

Organic Electronics
46 (2017) 14–21

Previous works with CVD polymer films in Dielectric

ARTICLES
PUBLISHED ONLINE: 9 MARCH 2015 | DOI: 10.1038/NMAT4233

Synthesis of ultrathin polymer insulating layers by initiated chemical vapour deposition for low-power soft electronics

Hanul Moon ^{1,2†}, Hyejeong Seong ^{2,3†}, Woo Cheol Shin ^{1,2†}, Won-Tae Park ⁴, Mincheol Kim ^{1,2}, Seungwon Lee ^{1,2}, Jae Hoon Bong ^{1,2}, Yong-Young Noh ⁴, Byung Jin Cho ^{1,2*}, Seunghyun Yoo ^{1,2*} and Sung Gap Im ^{1,2*}

a Initiator, Radical, Monomer

b V3O3 → pV3O3

c Normalized Intensity vs Energy loss (eV)

d Schematic of the device structure and TEM image of the cross-section.

e Normalized Intensity vs Distance (nm)

a Current-voltage characteristics of the device.

d Schematic of the device structure and TEM image of the cross-section.

e Normalized Intensity vs Distance (nm)

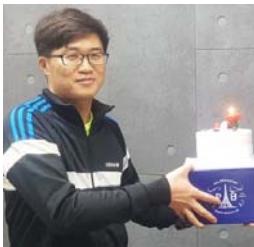
628 NATURE MATERIALS | VOL 14 | JUNE 2015



In addition to High dielectric constant, high capacitance, low leakage current, operation stability...etc

→ **Interfacial Optimization with f-Parylene**
 → **Obtaining InGaZnO (CVD based) semiconductor devices with Top or Bottom gate from**

Cowork with Prof. Kim H



2018

101. Hyung-Do Kim, Jong-Heon Kim, Kyung Park, Joseph Park*, and Hyun-Suk Kim*, "corresponding author, "Highly Stable Thin-Film Transistors based on Indium Oxynitride Semiconductor" (In preparation).

100. Jong-Heon Kim, Kyung Park, Su-Ho Cho, Yun-Cheng Park, Jung Hyun Kim, Chunjoong Kim, Il-Doo Kim, Joseph Park, and Hyun-Suk Kim*, "corresponding author Effect of annealing temperature on the interfacial interaction of $\text{LiNb}_3\text{Nb}_2\text{O}_8$ thin film cathode with stainless-steel substrate", *Thin Solid Films* (Under review).

99. Dae Seo Park, Won Seok Choi, Hyunil Kang, Sung-Wook Baek, Hyun-Suk Kim, Abul K. Azad, Junghwan Kim, and Hyun-Suk Kim*, "X-ray Photoelectron Spectroscopic Study of Impregnated Li_xCr_{1-x}Ni_xO₂ Anode Material for High Temperature-Operating Solid Oxide Fuel Cell", *Ceramics International* (Under review).

98. Kyung Park, Jong-Heon Kim, Hyun-woo Park, Ju-Hyeock Baek, Jonguk Bai, Kwon-Shik Park, Inyeong Kim, Kwon-Bum Chang, Hyun-Suk Kim*, and Jang-Yeon Kwon*, "corresponding author, "Highly Reliable Amorphous In-Ga-Zn-O Thin Film Transistors Through the Addition of Nitrogen Doping", *IEEE Transactions on Electron Devices* (Under review).

97. Ho-Hyun Heo, Jonghoon Yim, Hyun-Suk Kim, and Seungwoo Han, "Electron Effective Mass Increased by Intentional Impurities in Zinc Nitride: A Band-Gap Uncertainty Theory", *Advanced Materials* (Under review).

96. Jin Young Kim, Yu Kyung Park, Javier Martinez, Marta Tello, Ramon V. Martinez, Hyun-Suk Kim, Yun Tang, Emanuele Tosatti, Ricardo Garcia, and Francesco Stellacci, "Nano 'crystal radio' receivers as a femtoWatts tunable infrared room-temperature photodetectors", *Nature Materials* (Under review).

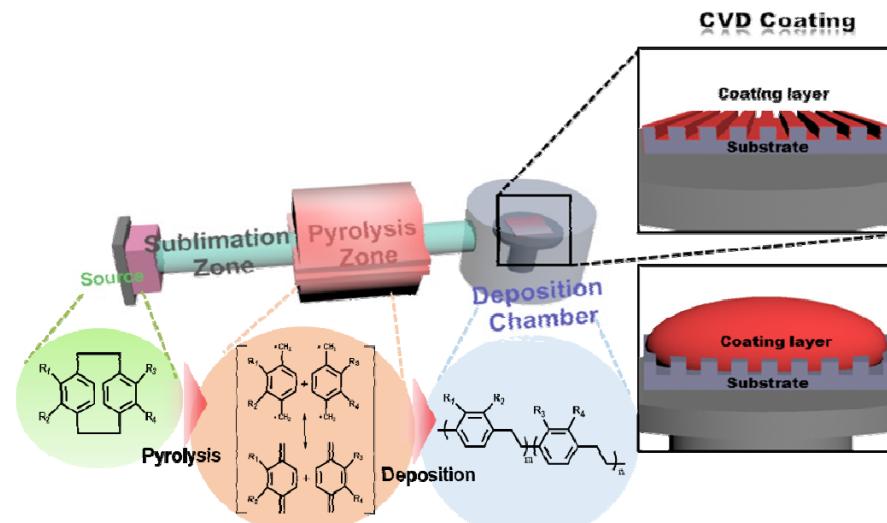
2017

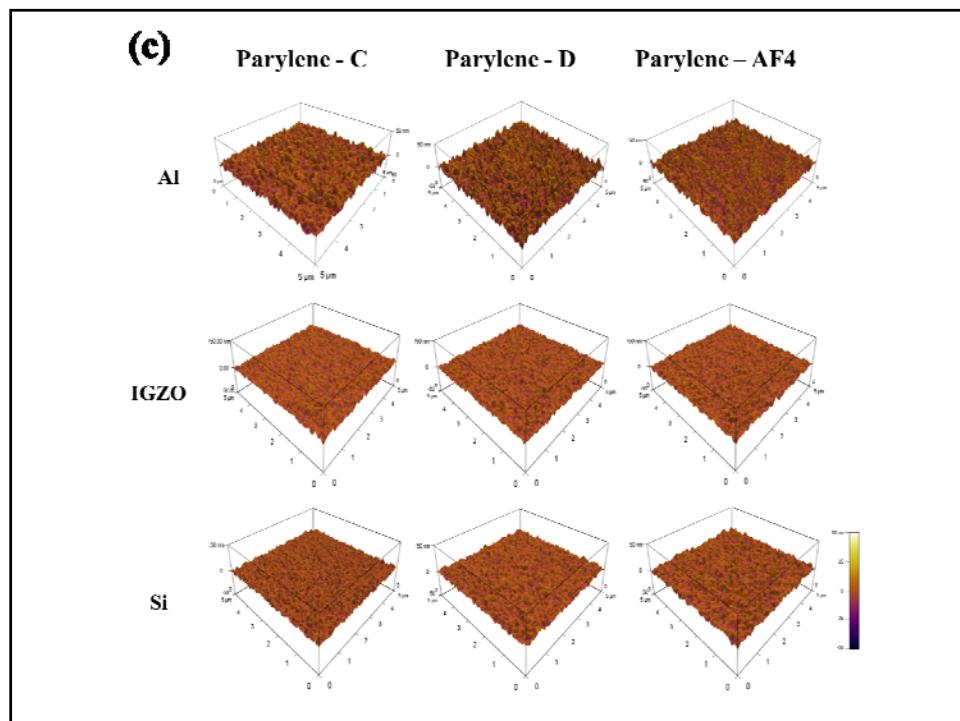
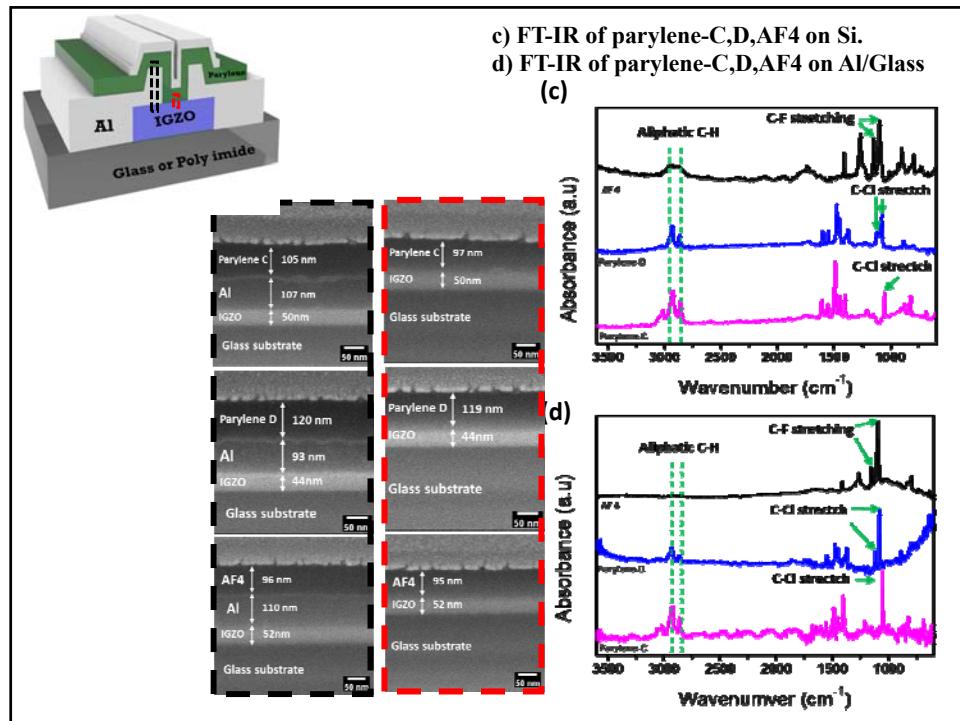
95. Jong-Heon Kim, Hyung-Do Kim, Dae-Gyu Yang, Hyun-Suk Kim*, "corresponding author, "High-Voltage $\text{LiNb}_3\text{Nb}_2\text{O}_8$ Thin Film Cathode Prepared by RF Sputtering", *ECS Transactions*, vol. 80 (Accepted).

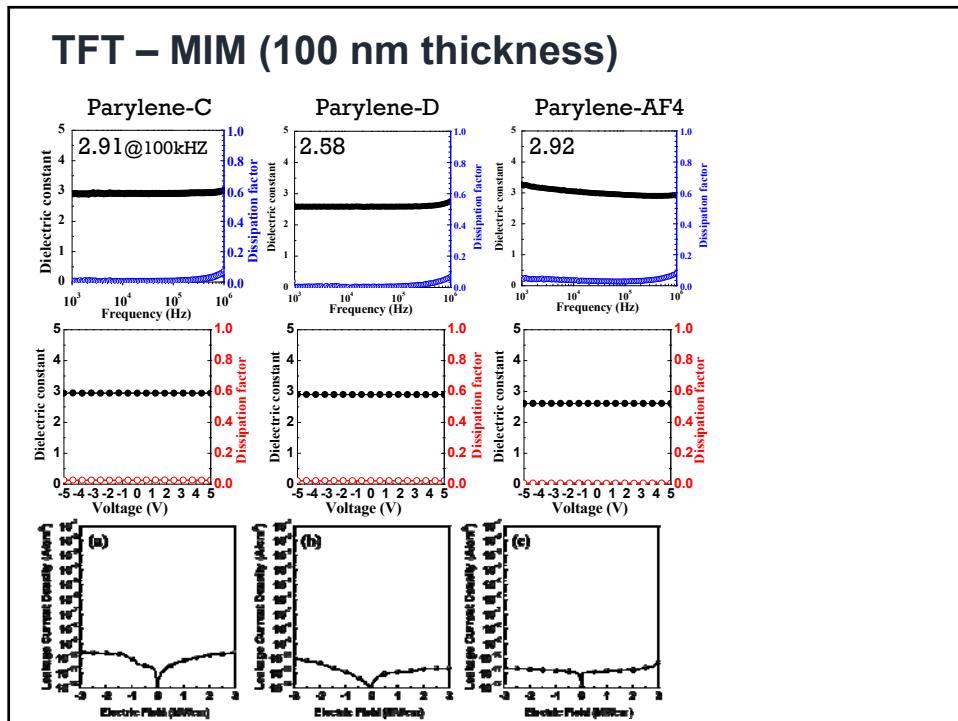
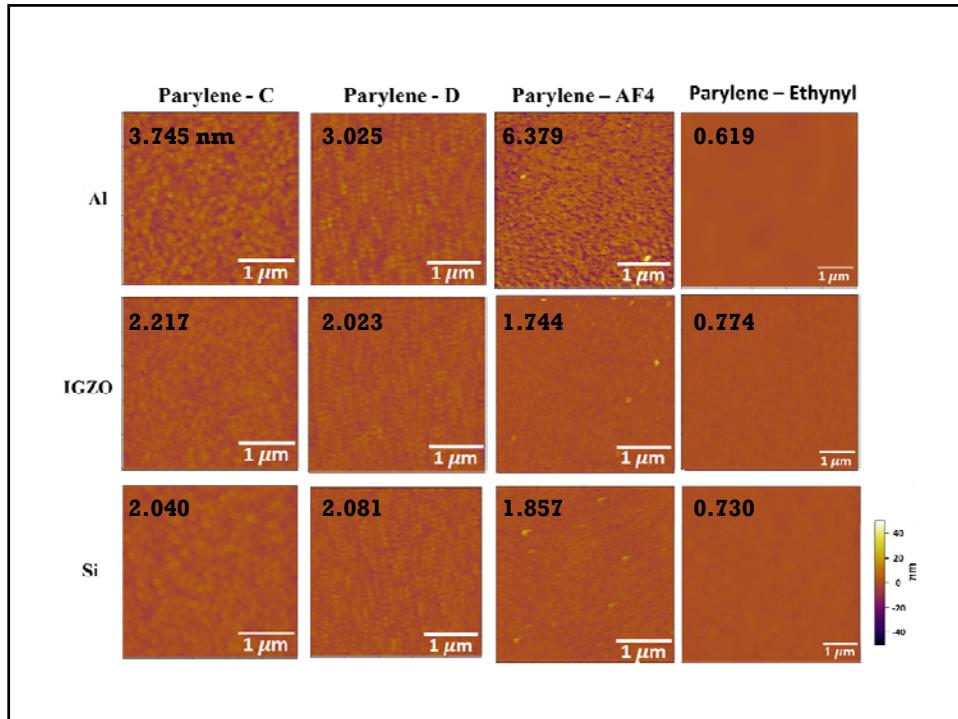
94. Na Liu, Jongyoon Baek, Seung Min Kim, Seongin Hong, Young Ki Huang, Yang Soo Kim, Hyun-Suk Kim*, Sunook Kim*, and Joseph Park*, "corresponding author, "Stability Improvement of High-Performance Multilayer MoS₂ Field-Effect Transistors", *ACS Applied Materials & Interfaces*, vol. 9, 42943-42950 (2017) [DOI 10%] [IF = 7.594]

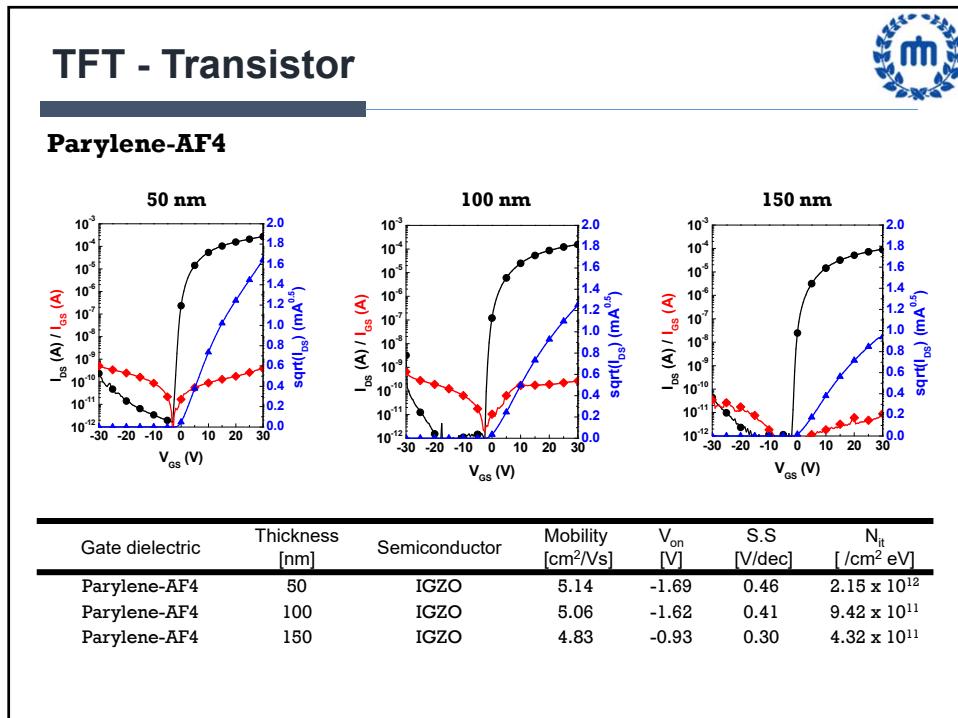
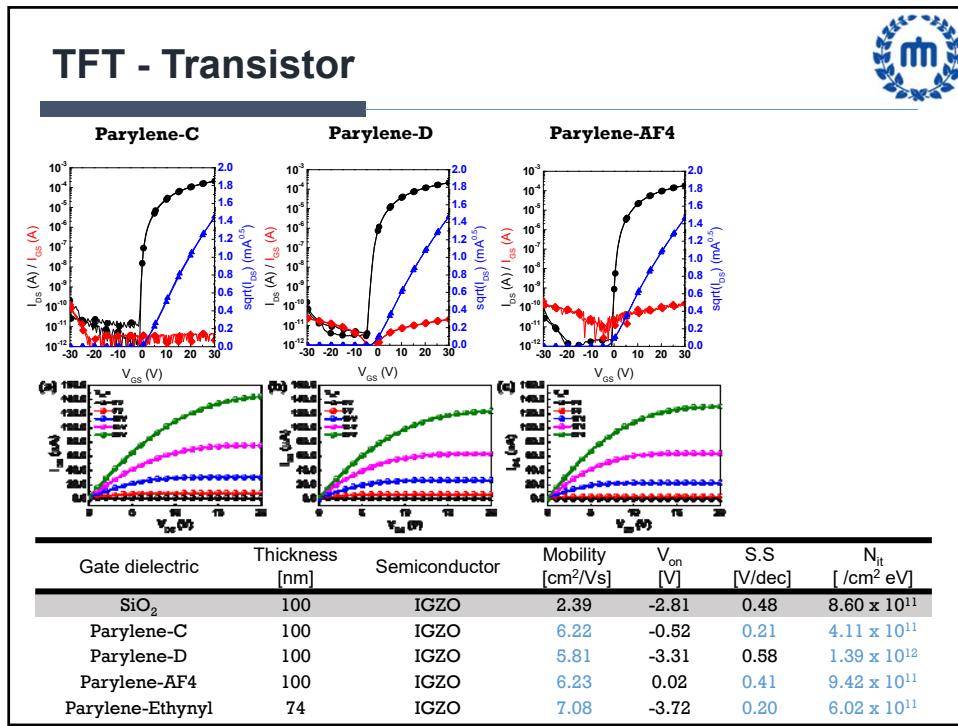
CNU 총남대학교 CHUNGJANG NATIONAL UNIVERSITY

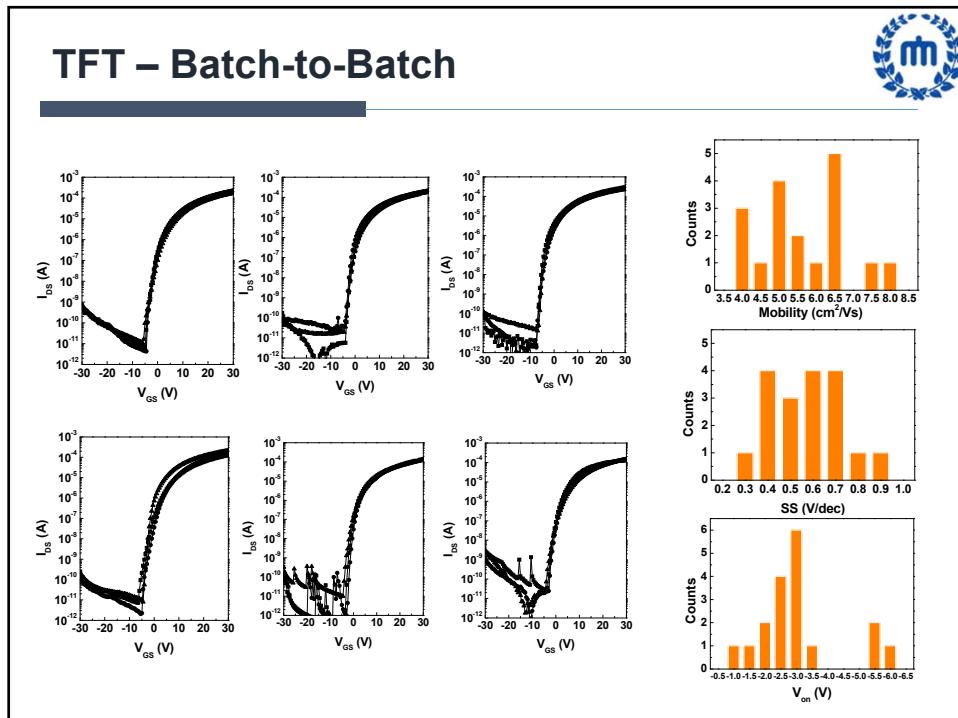
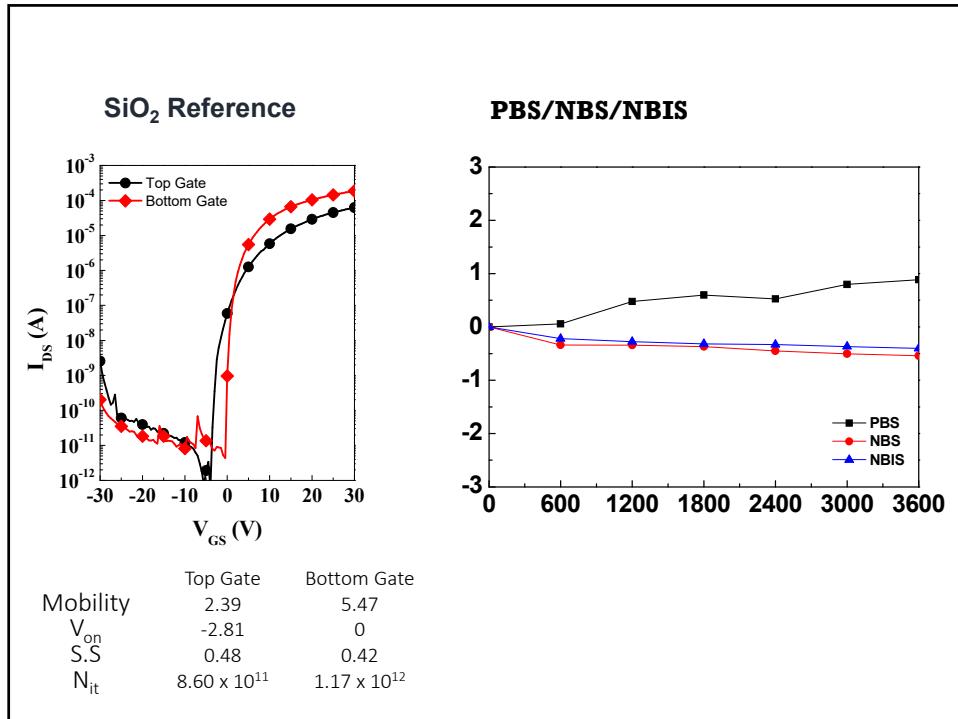
Schematic diagram of parylene coating with several monomer



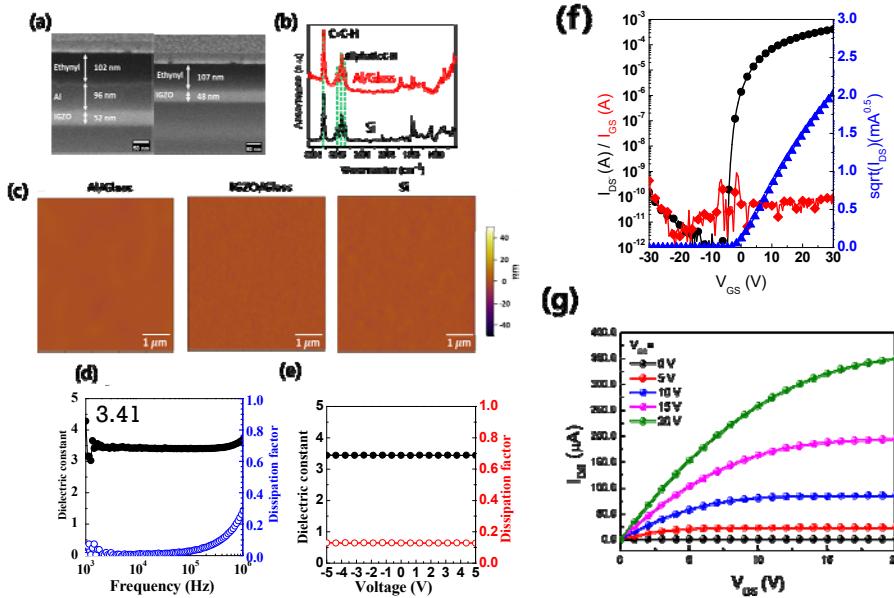




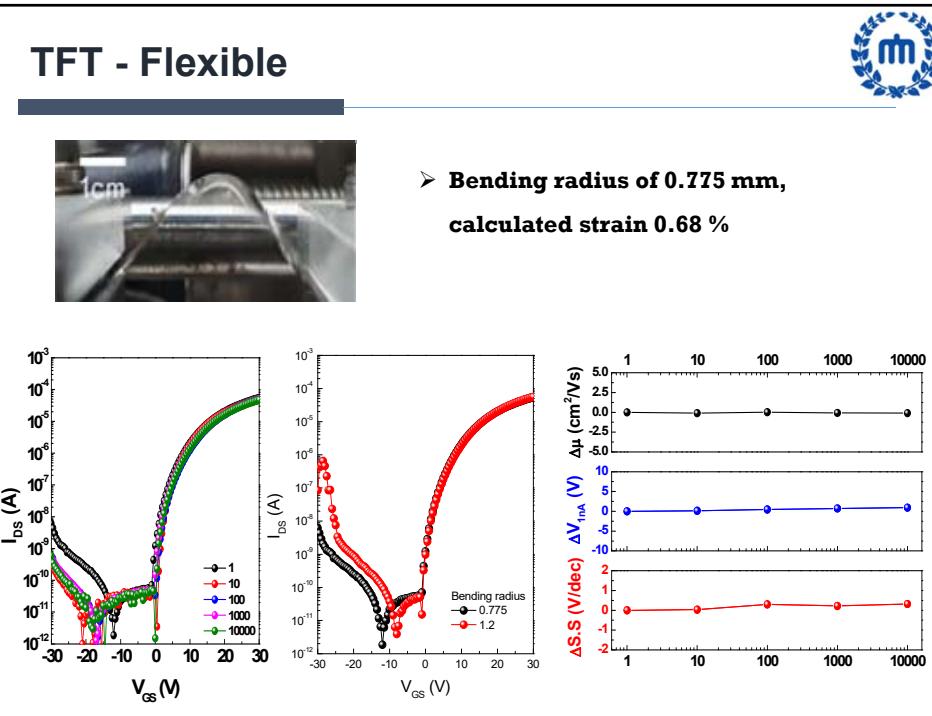


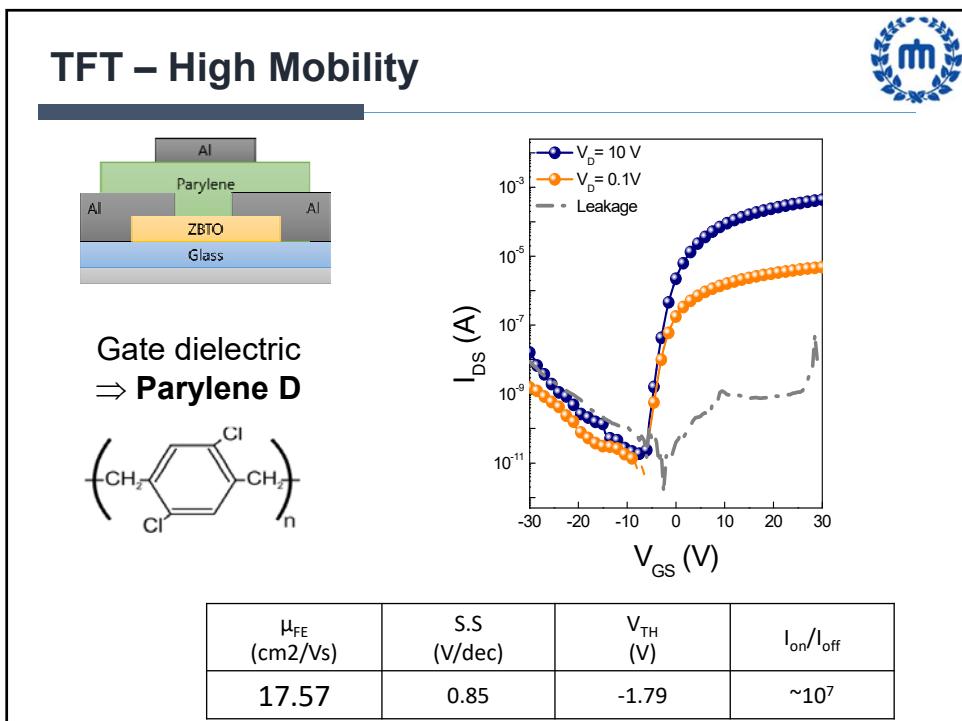


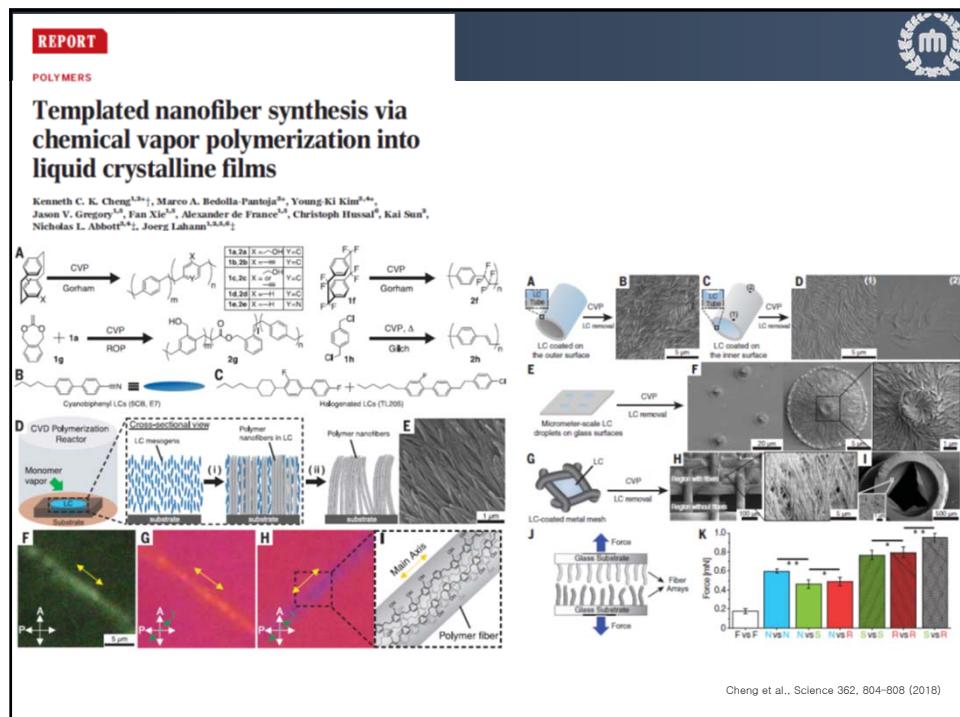
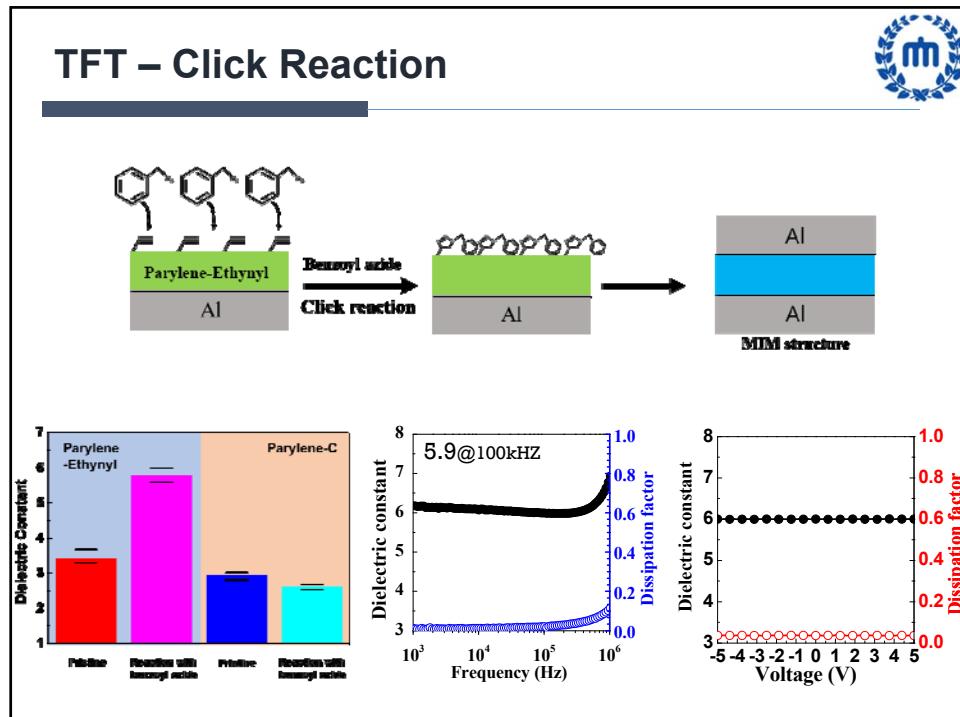
Parylene - ethynyl

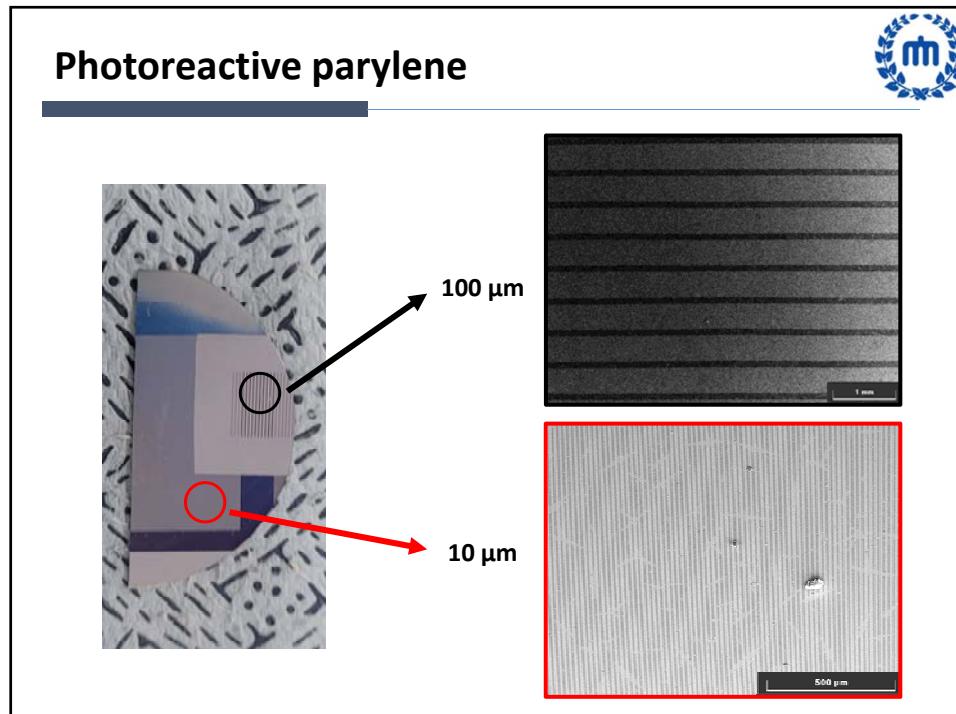
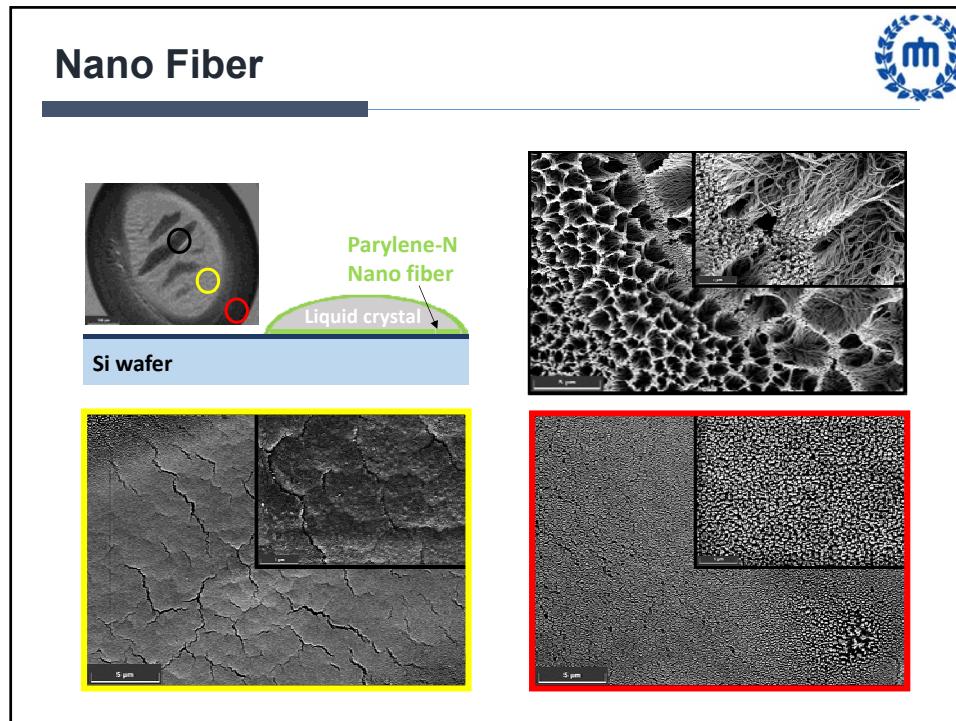


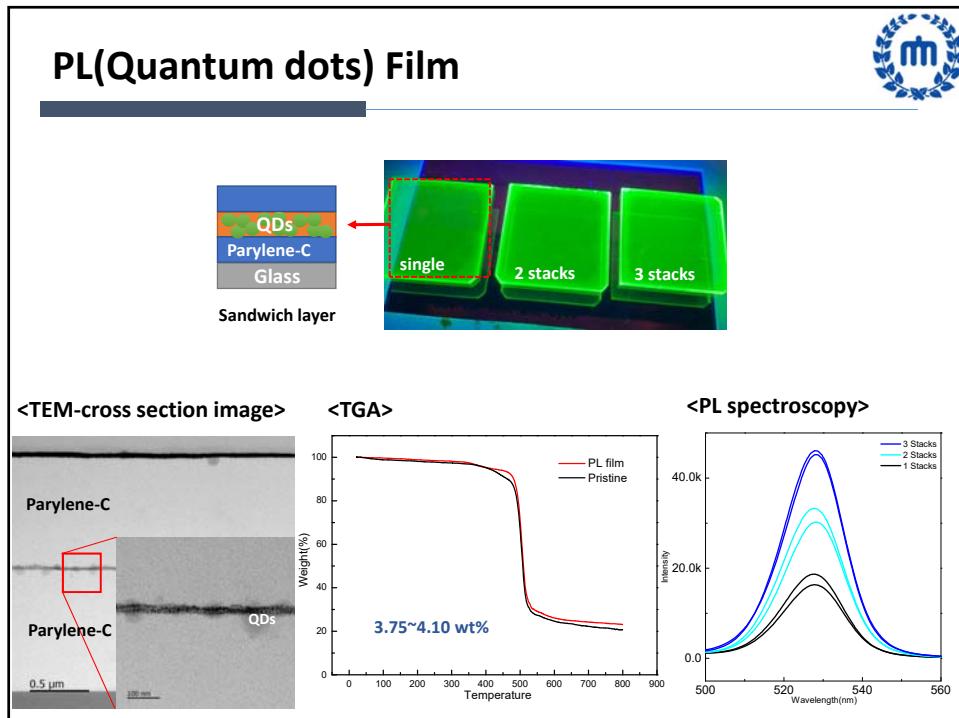
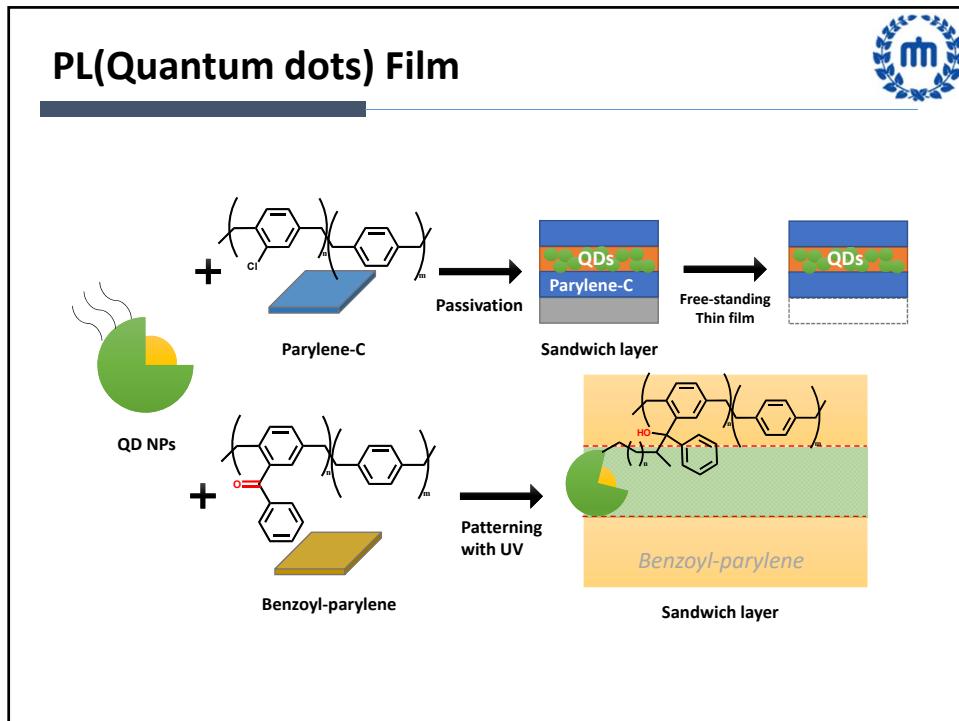
TFT - Flexible

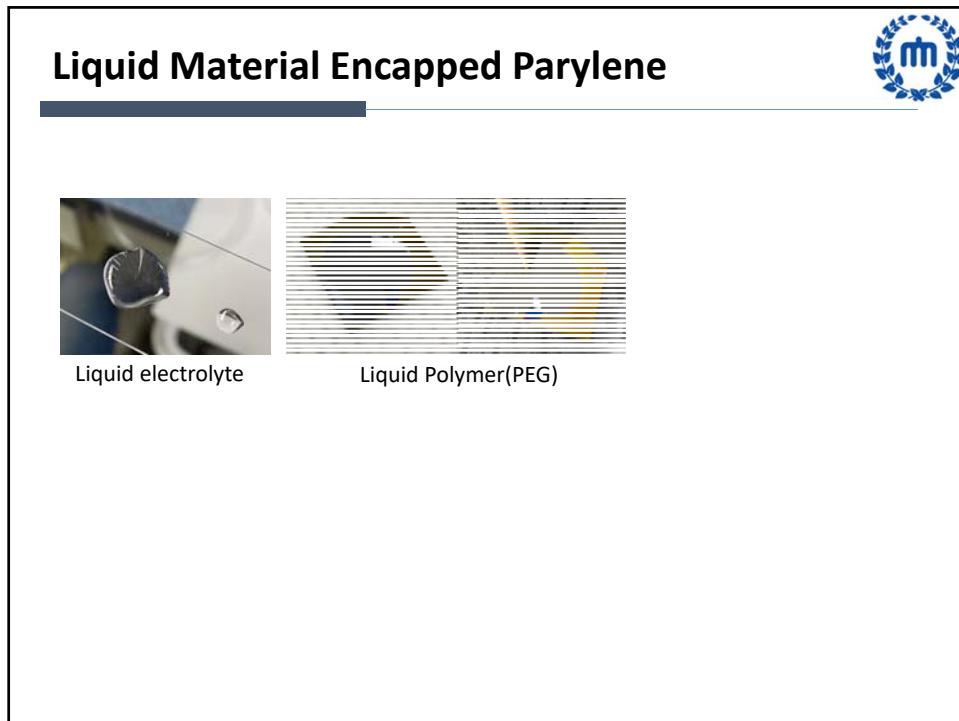












Paradox 형 Parylene 박막 필름

